

Notice of References Cited	Application/Control No. 10/600,715	Applicant(s)/Patent Under Reexamination SHARMA, VINOD	
	Examiner Stephen Elmore	Art Unit 2186	Page 1 of 1

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*	B	US-6,223,260	04-2001	Gujral et al.	711/145
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*	D	US-5,802,578	09-1998	Lovett, Thomas D.	711/147
*	E	US-5,796,980	08-1998	Bowles, James E.	711/144
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Lee et al., "Shared tag for MMU and cache memory," pp 77-80, CAS '97 Proceedings, Vol. 1, IEEE, October 1997.
*	V	Lee et al., "Indirectly-compared cache tag memory using a shared tag in a TLB," pp 1764-1766, Electronics Letters, Vol. 3, No. 21, October 1997.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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